

Notice of References Cited			Application/Control No. 09/705,678	Applicant(s)/Patent Under Reexamination BOGGS ET AL.	
			Examiner David J. Huisman	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-3,603,934	09-1971	Heath et al.	714/2
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hennessy and Patterson, "Computer Architecture - A Quantitative Approach, 2nd Edition," 1996, pp.152-154 and 253-258
	V	Johnson, "Superscalar Microprocessor Design," 1990, pp.133-134
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.